

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	((memory adj array) and (P adj channel adj transistors) and (cross adj coupled adj latch) and (power adj control adj circuitry) and (low adj voltage adj reference) and (data adj corruption adj mode) and counter and decoder).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/25 10:53
L2	68	cooker.inv.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/25 10:53
L3	310955	thomas.inv.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/25 10:53
L4	3	2 and 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/25 10:53